Electrical and electronic engineering

The following table summarizes the various internship models for the mandatory internship. It shows which status must be specified in the academic registration form (FRAC) when the internship takes place during the academic semester.
A correct FRAC status is required to ensure a smooth academic registration. Please read the table and notes below carefully.

Internships taking place during academic breaks should not be mentioned in FRAC.

Electrical and electronic engineering				
Internship models	Credits	Status to be specified in FRAC	Semester taken into account in the duration of the studies	Notes
SCS, 4 to 6 months	30	Internship	Yes	The internship (SCS) may only be undertaken after two semesters in the Master's cycle.
STAP, min. 8 semaines (max. 12 semaines), pendant la période des cours	Credited with the master's project	Internship	No	The internship (STAP) must in principle be undertaken after two semesters in the Master's cycle but before the Master's project. On the student's request, the program may give autorisation for an internship after the end of the Bachelor's cycle.
PDME (17 to 25 weeks)	30	Present	Yes	

Important note:
- if, during the internship, the student would like to register for a course, s/he must set the FRAC status to PRESENT.
- if the internship has not been validated by the internship coordinator, the semester when the internship takes place will be taken into account in the duration of the studies. In that case, please select « **Personal internship, not registered in the EPFL internship portal** ».
- the extension of the duration of the mandatory internship beyond one semester will be considered like a personal internship

SCS: Internship with separate credits obtained during the Master's cycle

STAP: Internship not related to the Master's project, credited with the Master's project

PDME: Master's project in industry