The Institute of Electrical Engineering (IEL) of EPFL selects and supports highly qualified candidates with their applications to the Eccellenza Professorial Fellowships of the Swiss National Science Foundation (SNSF). IEL welcomes very strong applications in all the research domains of our Institute, but this year, (2021 Call) additional support will be specifically provided for applications in the area of Digital VLSI Circuits and Systems.

We seek ambitious applicants with research interests in the broad area of digital VLSI circuits and systems. Specific topics of interest include, but are not limited to, architectures for machine learning and AI at the edge and in the cloud for intelligent systems and media processing, circuits and technologies for in-memory computing, high performance and low-power microprocessors and SoCs. Candidates are also considered in related fields such as CAD for VLSI, high-speed mixed-signal design, and technologies for digital systems integration such as high-speed serial links and 3D integration.

Applicants should have demonstrated an early-stage track record in their chosen scientific area, ideally with some independent high-profile publications and initial leadership experience within a research group. Their profile needs to comply with the SNSF requirements for the Eccellenza program.

This new position is aimed at reinforcing the EPFL team in the area of microelectronics and strengthen the link to computer architects and embedded systems research in the schools of engineering and computer science.

The prospective candidate will be expected to initiate an independent and creative research group and participate in undergraduate and graduate teaching in the areas of microelectronics and digital systems.

Applications should include a cover letter with a statement of motivation, curriculum vitae, list of publications and a 4 pages summary of their SNF Eccellenza research plan as well as a ½ page statement of teaching interests. Applicants should also provide the names and addresses of 3 referees.

Applications will be evaluated internally by a panel of EPFL professors working in related areas. The successful candidate(s) will receive the necessary support from the Institute of Electrical Engineering in their application for an SNSF Eccellenza Professorial Fellowship at EPFL according to the regulations available at http://www.snf.ch/en/funding/careers/eccellenza/Pages/default.aspx. Beyond the formal support, EPFL provides early feedback on the application, but the final application is the responsibility of the candidate and the final evaluation and selection are the responsibility of the SNSF.

Calendar & deadlines:
- Internal EPFL-IEL Application deadline: November 10th, 2020, by email to Prof. A. Burg.
- SNSF application deadline: February 1st, 2021.

Enquires and applications:
Prof. Andreas Burg
Internal Selection Committee Chair
e-mail: andreas.burg@epfl.ch

For additional information on EPFL, please consult the websites: www.epfl.ch, sti.epfl.ch, iel.epfl.ch.

EPFL is an equal opportunity employer and family-friendly university. It is committed to increasing the diversity of its faculty. It strongly encourages women to apply.